

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Isamu Kobori et al.

Serial No. : 10/623,581

Filed : July 22, 2003

Art Unit : 2812

Examiner : Stanetta Isaac

Confirmation No.: 6534

Notice of Allowance Date: March 24, 2008

Title : METHOD OF MANUFACTURING A SEMICONDUCTOR METHOD OF  
MANUFACTURING A THIN-FILM TRANSISTOR AND THIN-FILM  
TRANSISTOR

**MAIL STOP ISSUE FEE**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

RESPONSE TO NOTICE OF ALLOWANCE

In response to the Notice of Allowance mailed March 24, 2008, enclosed are a completed issue fee transmittal form PTOL-85b. Applicants have previously paid the \$1400 on March 14, 2006. Please charge the remainder amount of \$40 to Deposit Account No. 06-1050. Please apply any additional charges or credits to our Deposit Account No. 06-1050.

Respectfully submitted,

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Roberto J. Devoto  
Reg. No. 55,108

Date: 6/23/08

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